

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of			
3	Lewis B. Aronson et al.)	
Serial No.:	10/629,302) Art Unit	
Filed:	July 28, 2003) 2661	
Confirmation No.:	6438))	
For:	TRANSCEIVER MODULE AND INTEGRATED CIRCUIT WITH DUAL EYE OPENERS AND INTEGRATED LOOPBACK AND BIT ERROR RATE TESTING))))	

CERTIFICATE OF DEPOSIT UNDER 37 C.F.R. § 1.8

I hereby certify that the following documents are being deposited with the United States Postal Service as First Class Mail, postage prepaid, in an envelope addressed to: Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450, on the 24th day of February 2004.

- Transmittal for Information Disclosure Statement (3 pages)
- Information Disclosure Statement (2 pages)
- Form PTO-1449 listing 14 references (2 pages)
- A copy of each of the references list on the Form PTO-1449
- Postcard

Respectfully submitted,

CARL T. REED

Attorney for Applicant Registration No. 45,454

Customer No. 022913

CTR:dfw

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PATENT APPLICATION Docket No: 15436.247.2.1.4

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TRANSMITTAL FOR INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

35 U.S.C. § 120.

Sir:

Transmitted herewith for filing and pursuant to 37 C.F.R. § 1.97 is an Information Disclosure Statement, which includes the following statements, if any, required variously by 37 C.F.R. § 1.98:

 Statement of relevance of selected cited references not in the English language which are not translated.
 Statement that selected cited references are substantially cumulative of an enclosed or previously submitted reference.
 Statement that selected cited references were previously cited by or submitted to the United States Patent and Trademark Office in a

prior application which is relied upon for an earlier filing date under





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Serial No.:	10/629,302) Art Uni) 2661
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<u>UNDER 37 C.F.R. § 1.97</u>

Commissioner for Patents PO Box 1450 Alexandria, Virginia 22313-1450

Sir:

Please find, pursuant to 37 C.F.R. § 1.98(a)(1), the enclosed Form PTO-1449 which contains a list of all patents, publications, or other items that have come to the attention of one or more of the individuals designated in 37 C.F.R. § 1.56(c). While no representation is made that these references may be "prior art" within the meaning of that term under 35 U.S.C. §§ 102 or 103, the enclosed listed references are disclosed so as to fully comply with the duty of disclosure set forth in 37 C.F.R. § 1.56.

Moreover, while no representation is made that a specific search of office files or patent office records has been conducted or that no better art exists, the undersigned attorney of record believes that the enclosed art is the closest to the claimed invention (taken in its entirety) of which the undersigned is presently aware, and no art which is closer to the claimed invention (taken in its entirety) has been knowingly withheld.

In accordance with 37 C.F.R. §§ 1.97 and 1.98, a copy of each of the listed references or relevant portion thereof is also enclosed.

Dated this 24th day of February 2004.

Respectfully submitted,

CARL T. REED

Attorney for Applicant Registration No. 45,454 Customer No. 022913

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	A.	Addit	ional Materials Required Due to Content of Information Disclosure Statement	
Staten	Transmitted are the following documents in addition to the Information Disclosure statement as required variously under 37 C.F.R. § 1.98:			
	<u>X</u>	Form PTO-1449 listing 14 references submitted for consideration.		
	<u>X</u>	A cop	y of each of the references listed on the Form PTO-1449.	
			sh translations of () of the references listed on the Form PTO-1449 are not in the English language.	
		Copie applic	s of the following documents from the prosecution of a previous, related ation:	
			Form PTO-1449 AND INFORMATION DISCLOSURE STATEMENT; and	
			Form PTO-892	
	B.	Additi Staten	ional Materials Required Due to Timing of Filing of Information Disclosure nent	
follow	The transmitted Information Disclosure Statement is being filed within one (1) of the following four (4) time periods:			
	I.	<u>X</u>	Prior to the later of either three (3) months following the filing date or the mailing of a first Office Action. Accordingly, no materials other than those listed above are enclosed.	
	II.		Following the latter of either three (3) months following the filing date or the mailing of a first Office Action, but before the mailing of a final Office Action or a Notice of Allowance. Accordingly, to secure consideration thereof, one (1) of the following is also enclosed:	
			Promptness Certification; or	
			Check No in the amount of \$ constituting the submission fee set forth in 37 C.F.R. § 1.17(p).	
	III.		After the mailing of a Notice of Allowance, but before payment of the Issue Fee. Accordingly, in order to secure consideration thereof, each of the following are also enclosed:	
			Promptness Certificate;	
			Petition for Consideration: and	

			Check No. in the amount of \$ constituting the petition fee set forth in 37 C.F.R. § 1.17(i)(1).	
	IV.		After payment of the Issue Fee. Accordingly, in order to secure consideration thereof, each of the following are also enclosed:	
			Petition to Withdraw from Issue; and	
			Check No in the amount of \$ constituting the petition fee set forth in 37 C.F.R. § 1.17(i)(1).	
	C.	<u>Fees</u>		
The Commissioner is hereby authorized to charge payment of or any deficiency in the following fees associated with this communication, or to credit any overpayment thereof, to Deposit Account No. 23-3178. A duplicate copy of this letter is enclosed.				
	<u>X</u>	Any fee required in relation to filing of this letter or any documents transmitted therewith.		
		The submission fee set forth in 37 C.F.R. § 1.17(p) in the event that 37 C.F.R. § 1.97(c) applies and the Examiner is not satisfied that any Promptness Certificate submitted meets the requirements of 37 C.F.R. § 1.97(e).		
		The submission fee set forth in 37 C.F.R. § 1.17(p).		
	•	The petition fee set forth in 37 C.F.R. § 1.17(i)(1).		
	Dated this 24 th day of February 2004.			

Respectfully submitted,

CARL T. REED

Attorney for Applicant Registration No. 45,454 Customer No. 022913

CTR:dfw Enclosures DFW0000009590V001 Form PTO-1449

Sheet 1 of 2

Confirmation No.: 6438

Applicant:

Lewis B. Aronson et al.

Serial No.:

10/629,302

July 28, 2003

Att'y Docket No.: 15436.247.2.1.4

Group: 2661

Filing Date: For:

TRANSCEIVER MODULE AND INTEGRATED CIRCUIT WITH DUAL EYE OPENERS

AND BIT ERROR RATE TESTING



Examiner:

INFORMATION DISCLOSURE CITATIONS MADE BY APPLICANT

U.S. Patent Documents

•		O.S. I atent Documents	•
Examiner <u>Initial*</u>	Document Number	Issue <u>Date</u>	<u>Name</u>
1	5,854,704	12/29/1998	Grandpierre
2	6,256,127 B1	07/03/2001	Taylor
3	4,932,038	06/05/1990	Windus
4	2002/0021468 A1	02/21/2002	Kato et al.
5	2002/0060824 A1	05/23/2002	Liou et al.
6	2003/0053170 A1	03/20/2003	Levinson et al.
Examiner	Document	Foreign Patent Documents Publication	Country or
<u>Initial</u> *	Number	Date	Patent Office <u>Translation</u>
Other Documents (including author, title, pertinent pages, etc.)			
Examiner <u>Initial</u> *			
7	LXT16706/16707 SerDes Chipset, Intel Products, www.intel.com/design/network/products/optical/phys/lxt16706.htm, April 19, 2002.		
8	LXT35401 XAUI-to-Quad 3.2G Transceiver, Intel Products, www.intel.com/design/network/products/optical/phys/lxt35401.htm, April 19, 2002.		
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Date Considered:

Form PTO-1	· · · -	Sheet 2 of 2	
Applicant:	Lewis B. Aronson et al.	Confirmation No.: 6438	
Serial No.:	10/629,302	Att'y Docket No.: 15436.247.2.1.4	
Filing Date: For:	July 28, 2003	Group: 2661	
roi.	TRANSCEIVER MODULE AND INTEGRATAND BIT ERROR RATE TESTING	TED CIRCUIT WITH DUAL EYE OPENERS	
	AND DIT EXICIT INTE		
9	Texas Instruments User's Guide, TLK2201 Sea Solutions, July 2000.	les EVM Kit Setup and Usage, Mixed Signal DSP	
10	Texas Instruments User's Guide, TLK1501 Serdes EVM Kit Setup and Usage, Mixed Signal Products, June 2000.		
11	National Semiconductor DS92LV16 Design G	uide, Serializing Made Simple, February 2002.	
12	Vaishali Semiconductor, Fibre Channel Transe	ceiver, VN16117, MDSN-0002-02, 08/09/2001.	
13	Fairchild Semiconductor, Application Note 77,	CMOS, the Ideal Logic Family, January 1983.	
14	Analog Target Specification, Annex 48B, Publ	ished by IEEE New York, 05/2001, pp. 6-14.	
·	References Cited by	Applicants	
Section 609 o Information D other item of readily available	f the Manual of Patent Examining Procedure and 37 isclosure Statement, Form PTO-1449 shall be accominformation and a translation of the pertinent portion.	cluntary, the procedure is governed by the guidelines of C.F.R. §§ 1.97 and 1.98. To be considered a proper panied by a copy of each listed patent or publication or ons of foreign documents (if an existing translation is each reference not in the English language, and should	
place their inition conformance wany reason whif it is not in c	tials adjacent the citations in the spaces provided on with the guidelines which may have been considered tether or not the citation is in full conformance with the conformance with the guidelines AND has not been conter, will be returned to the applicant with the next conter,	nance with 37 C.F.R. § 1.98 and MPEP Sec. 609 and this form. Examiners will also initial citations not in . A reference may be considered by the Examiner for the guidelines. A line will be drawn through a citation onsidered. A copy of the submitted form, as reviewed mmunication. The original of the form will be entered	
	citation initialed by the Examiner will be printed on the on Form PTO-892.	ne issued patent in the same manner as references cited	
The rewill be used be Action Form F	by the Examiner in the same manner as Examiner's	Applicant's reference 1, Applicant's reference 2, etc.) reference designations "A," "B," "C," etc. on Office	
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Examiner:	Date Con	sidered:	

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.